

भारतीय प्रौद्योगिकी संस्थान गांधीनगर

पालज, गांधीनगर, गुजरात 382 055 INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR PALAJ, GANDHINAGAR, GUJARAT 382 055

Dr. Madhav K. Pathak

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Advertisement for Junior Research Fellow (JRF) Position Electrical Engineering, IIT Gandhinagar

Project Funding Agency: Ministry of Electronics and Information Technology (MeitY)

Project: NSF-MeitY: Fundamental Realizable Limit Vibration Energy Harvesting: Design, Analysis, and Implementation

Required Qualification/Skills:

- Masters (MTech/ME) or Bachelors (BTech/BE) in Electrical/Electronics or related disciplines with UGC-NET/GATE/other centralized examination qualification as applicable.
- Strong embedded programming skills
- Proficiency with circuit design and simulation
- Experience with PCB Designing, soldering, and Prototyping
- Familiarity with basic lab equipment like DMM and Oscilloscope
- Hands-on experience in electrical characterization and board level testing will be an added advantage.

Duration: Initially 12 months; extendable based on performance and availability of funds

Remuneration: INR 37,000/- per month + HRA (as per norms, if applicable)

Application Procedure: Apply via Google Form: https://forms.gle/p3jwu16nQhfEEezY7

Required documents for application to be uploaded via above Google Form: 1) CV listing educational degrees with percentage/CPI, internships, work experience, projects, research publications, and any other relevant information; 2) GATE/UGC scorecard

Priority deadline: 14th August 2025. This is a rolling advertisement until the position is filled however applications received on or before Aug 14 will be considered on priority.

The selection will be based on the online/offline interview and the shortlisted candidates will be intimidated in advance the date, time, and mode of the interview via email.

Contact: Madhav Pathak at madhav.pathak@iitgn.ac.in