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INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR (IITGN)

Advertisement for Junior Research Fellow

19.09.2025

Job Description

Applications are invited for two temporary position of **Junior Research Fellow** for the research project on "**Secure and Energy efficient Mixed domain Compute in memory-based AI accelerator Chip for Edge applications**" funded Ministry of Electronics and IT (MEITY) through the C2S project.

Remuneration: INR 37000/- (consolidated) p.m. + INR 8,880 (HRA)

HRA will be provided as per institute norms if accommodation is not available within the institute.

Duration

The candidates will be offered a position for 12 months, which will be extended by 1 year based on the satisfactory performance of the candidate and availability of funds.

Essential Qualifications

BTech / BE / MTech / ME degree in Electrical/Electronics/Electronics and communications/Electronics and Instrumentation/Computer Science and Engineering/Computer Science or related disciplines. The candidate should be GATE qualified (if applicable). The candidate should have a good academic record throughout and the percentage/grade points with respect to the academic qualifications will be a minimum of 60% or equivalent in Graduation and above and 55% or equivalent grade in 10th and 12th.

Project Description

The candidate is expected to have excellent understanding of Digital Circuits, Analog circuits, VLSI design, etc. and should have a thorough exposure to relevant VLSI tools and techniques. Candidates with excellent programming skills C/C++/Python, Verilog/System Verilog or equivalent, etc. and candidates with interest in computer architecture and having knowledge of system simulators will be given preference. Students with exposure of ADCs, DACs, etc. will be preferable.

Application procedure: Interested candidates can apply here. For any query, please send an email to joycee@iitgn.ac.in

Interview will be conducted in the **online** mode.

No TA/DA will be provided for interview.

LAST DATE : 7th October, 2025